ELECTRONIC OVERSPEED GOVERNOR FOR ELEVATORS

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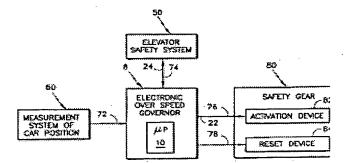


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Abstract of WO0039015

An electronic overspeed governor (8) for preventing elevator overspeed by enabling safety devices. The electronic overspeed governor includes a microprocessor assembly (10) designed to interface with existing sensors, detectors, components, and safety equipment of current operational and production elevator systems. The microprocessor assembly receives very accurate position measurements from a position measurement system (60). The microprocessor receives configuration information from a ROM which contains data specific to the particular model of elevator and other installation specific parameters. The software calculates elevator speed based on successive position data. If an overspeed or near overspeed condition occurs, the microprocessor generates the appropriate outputs to be conveyed to the elevator safety system. The safety system activates devices to arrest the overspeed condition. The microprocessor and associated components provide an overspeed governor which is faster, more accurate and reliable than the prior art.



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